

ABSTRACT OF THE DISCLOSURE

A display driver multiplexes gray-scale data captured by a data latch and outputs a data signal to a comb-tooth distributed data line. The data latch includes a gray-scale bus to which the gray-scale data for first to third color components is supplied corresponding to the arrangement order of the data lines. A first data latch, a second data latch, a first shift register, a second shift register, a first clock signal line, and a second clock signal line are N multiplexed. A multiplexer multiplexes N sets of gray-scale data captured by the first and second data latches. The display driver outputs the data signal corresponding to first or second multiplexed data to a data signal supply line.